

Web Results 1 - 10 of about 448 for **distributed dynamic functionality platforms integrat**

Scholarly articles for **distributed dynamic functionality platforms integrated circuit design isochronous fabric reconfigurable core embedded programmable logic map logic function**



[Socket-based design using decoupled interconnects](#) - by Wingard - 2 citations

[The DTM Protocol—Design and Implementation](#) - by Bohm - 6 citations

[RASSP Architecture Guide](#) - by Sanders - 0 citations

Magnetoresistive memory for a complex programmable logic device ... a map expressing logic function of The plurality of platforms, ... a reconfigurable core, a DSP, embedded programmable logic, and The like, ...

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Fresh Patents-Magnetoresistive memory for a complex programmable ...

... as a function of the dynamic changes in ... each platform, MRAM may be distributed through

the ... improved power consumption, power management functionality, as well ...

[www.freshpatents.com/ Magnetoresistive-memory-for-a-complex-programmable-logic-device-dt20041104ptan200402...](http://www.freshpatents.com/Magnetoresistive-memory-for-a-complex-programmable-logic-device-dt20041104ptan200402...) - 38k - Supplemental Result - [Cached](#) - [Similar pages](#)

[ftp.sunsite.ualberta.ca/Documentation/Gnu/vera-1.6...](ftp://sunsite.ualberta.ca/Documentation/Gnu/vera-1.6...)

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--LEGAL - acron.vera is based on a special GNU edition of VERA, a ...

... Distributed Function Terminal (IBM) DFT - Distributed Function Terminal DFU

... 1 (DEC), "PL/1" PLA - Programmable Logic Array PLATO - Programmed Logic ...

web.mit.edu/afs/athena/user/w/c/wchuang/www/unix/Unix/Acronyms.txt - 296k -

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[PDF] SSttuuddyyRReeppoorrt tooffSSDDRRTTeeccchhnn oollooggy & & SSyyss

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processing and reconfigurable logic enable more flexible design options today than that ... Dynamic selection of band, mode, and diversity as a function of ...

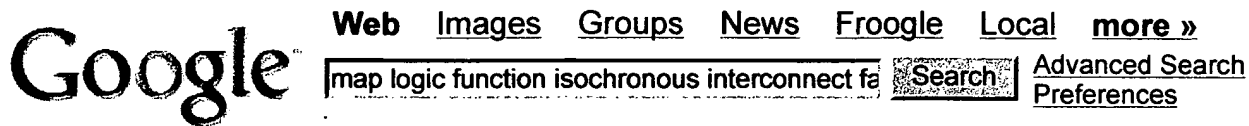
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[PDF] RASSP Architecture Guide

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... exercised overall control of the function of the ... The use of a distributed architecture has led to ... implemented using the Hughes Common Integrated Processor (CIP ...

eda.org/rassp/documents/sanders/arch_guide_b.pdf - Supplemental Result - [Similar pages](#)



Web Results 1 - 10 of about 156 for **map logic function isochronous interconnect fabric**

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a map expressing **logic function** of The **plurality** of **platforms**, ... a **reconfigurable core**, a **DSP**, **embedded programmable logic**, and The like, ...
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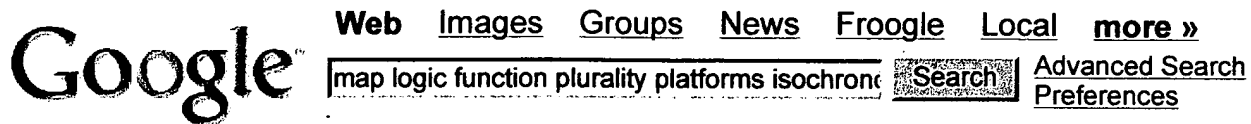
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Core functionality of JVTOS is provided by a so-called application sharing ...
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